

CLASSIFICATION	Subclass
Class	

PATENT NUMBER

**U.S. UTILITY Patent Application**

<p>O.I.P.E.</p> <p>SCANNED 105-4 O.A. An</p>	<p>PATENT DATE</p>
--	--------------------

APPLICATION NO. 09/939870	CONT/PRIOR F	CLASS 257	SUBCLASS 241 056.000	ART UNIT 2812 2036	EXAMINER SMA/K
------------------------------	-----------------	--------------	-------------------------	--------------------------	-------------------

Yoko Hayashida  
Kiminori Hayano  
Hiroshi Furuta

EXAMINER

~~S. M. A. A.~~

Nguyen, D

7/2/2011

Semiconductor integrated circuit device and method for designing the same

PTO-2040  
12/89**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> The claim of this patent has been disclaimed.	<b>DRAWINGS</b> Sheets Draw.      Figs. Draw.      Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims      Print Claim for O.G.	
	(Assistant Examiner)      (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Primary Examiner)      (Date)			<b>ISSUE FEE</b> Amount Due      Date Paid	
	(Legal Instruments Examiner)      (Date)			<b>ISSUE BATCH NUMBER</b>	

**WARNING:**  
The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A  
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM  
(Attached in pocket on right inside flap)